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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number 10/681481

Filing Date October 8, 2003

First Named Inventor Farrar, Paul

Group Art Unit 1746

Examiner Name Unknown

Sheet 1 of 1

Attorney Docket No: 1303.112US1

US PATENT DOCUMENTS

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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		WOLF, "SILICON PROCESSING FOR THE VLSI ERA", Process Integration, Vol. 1, (1989), 169-171	

EXAMINER

DATE CONSIDERED

01/10/07

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached.